



S/N 10/075484

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Arup Bhattacharyya Examiner: Trong Phan
Serial No.: 10/075484 Group Art Unit: 2818
Filed: February 12, 2002 Docket: 1303.043US1
Title: ASYMMETRIC BAND-GAP ENGINEERED NONVOLATILE MEMORY
DEVICE

COMMUNICATION CONCERNING RELATED APPLICATION(S)

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Applicant would like to bring to the Examiner's attention the following related application(s) in the above-identified patent application:

<u>Serial/Patent No.</u>	<u>Filing Date</u>	<u>Attorney Docket</u>	<u>Title</u>
09/945507	August 30, 2001	1303.014US1	FLASH MEMORY WITH LOW TUNNEL BARRIER INTERPOLY INSULATORS
09/944985	August 30, 2001	1303.023US1	SCALABLE FLASH/NV STRUCTURES & DEVICES WITH ENHANCED ENDURANCE
10/232848	August 30, 2002	1303.073US1	ONE-DEVICE NON-VOLATILE RANDOM ACCESS MEMORY CELL

Respectfully submitted,

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By Applicant's Representatives,

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CERTIFICATE UNDER 37 CFR 1.8: The undersigned hereby certifies that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail, in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on this 18th day of February, 2004.

Name

Signature